

## **STPS5H100**

## High voltage power Schottky rectifier

### Main product characteristics

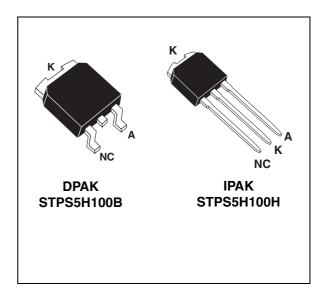
I <sub>F(AV)</sub>	5 A
V <sub>RRM</sub>	100 V
T <sub>j</sub> (max)	175° C
V <sub>F</sub> (max)	0.61 V

### Features and benefits

- Negligible switching losses
- High junction temperature capability
- Low leakage current
- Good trade off between leakage current and forward voltage drop
- Avalanche specification

### **Description**

This high voltage Schottky barrier rectifier is packaged in DPAK and IPAK, and designed for high frequency miniature switched mode power supplies such as adaptators and on board DC to DC converters.



#### **Order codes**

Part number	Marking
STPS5H100B	S5H100
STPS5H100B-TR	S5H100
STPS5H100H	S5H100H

Table 1. Absolute ratings (limiting values)

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Symbol	Parameter	Value	Unit	
$V_{RRM}$	Repetitive peak reverse voltage		100	V
I <sub>F(RMS)</sub>	RMS forward voltage		10	Α
I <sub>F(AV)</sub>	Average forward current	$T_c = 165^{\circ} \text{ C } \delta = 0.5$	5	Α
I <sub>FSM</sub>	Surge non repetitive forward current	t <sub>p</sub> =10 ms sinusoidal	75	Α
I <sub>RRM</sub>	Repetitive peak reverse current $t_p = 2 \mu s F = 1 \text{ KHz}$		1	Α
I <sub>RSM</sub>	Non repetitive peak reverse current $t_p = 100 \mu s$ square		2	Α
P <sub>ARM</sub>	Repetitive peak avalanche power $t_p = 1 \mu s$ $T_j = 25^{\circ}$ C		7200	W
T <sub>stg</sub>	Storage temperature range		-65 to + 175	°C
Tj	Maximum operating junction temperature <sup>(1)</sup>		175	°C
dV/dt	Critical rate of rise of reverse voltage		10000	V/µs

<sup>1.</sup>  $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$  condition to avoid thermal runaway for a diode on its own heatsink

**Characteristics STPS5H100** 

#### **Characteristics** 1

Table 2. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case	2.5	°C/W

Table 3. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I <sub>R</sub> <sup>(1)</sup> Reverse I	Reverse leakage current	T <sub>j</sub> = 25° C	$V_R = V_{RRM}$			3.5	μΑ
'R`	neverse leakage current	T <sub>j</sub> = 125° C			1.3	4.5	mA
	V (2) Farmand valtage dues	T <sub>j</sub> = 25° C	I <sub>F</sub> = 5 A			0.73	
V <sub>E</sub> (2)		T <sub>j</sub> = 125° C			0.57	0.61	V
V <sub>F</sub> <sup>(2)</sup> Forward voltage drop	T <sub>j</sub> = 25° C	I <sub>F</sub> = 10 A			0.85	V	
		T <sub>j</sub> = 125° C	11 <sub>F</sub> = 10 A		0.66	0.71	

<sup>1.</sup> Pulse test:  $tp = 5 \text{ ms}, \delta < 2\%$ 

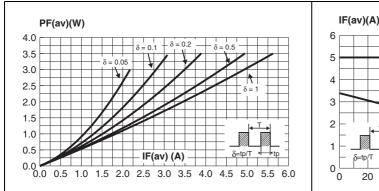
To evaluate the conduction losses use the following equation: P = 0.51 x  $I_{F(AV)}$  + 0.02 $I_{F}^{2}$ <sub>(RMS)</sub>

$$P = 0.51 \times I_{F(AV)} + 0.02I_{F}^{2}_{(RMS)}$$

<sup>2.</sup> Pulse test: tp = 380  $\mu$ s,  $\delta$  < 2%

STPS5H100 Characteristics

Figure 1. Average forward power dissipation Figure 2. Average forward current versus versus average forward current ambient temperature ( $\delta = 0.5$ )



IF(av)(A)

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Rth(j-a)=Rth(j-a)=Rth(j-c)

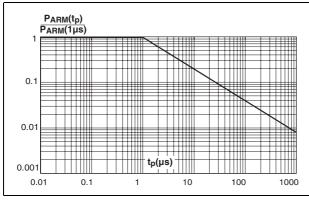
Rth(j-a)=80°C/W

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Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature



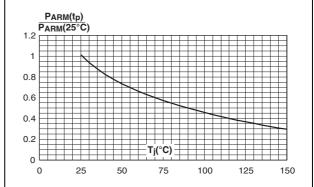
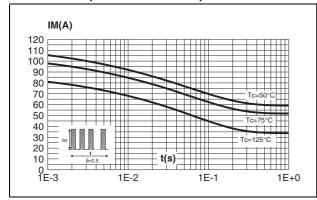
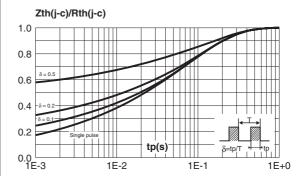


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values)

Figure 6. Relative variation of thermal impedance junction to case versus pulse duration

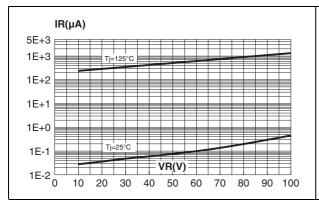




Characteristics STPS5H100

Figure 7. Reverse leakage current versus reverse voltage applied

Figure 8. Junction capacitance versus reverse voltage applied (typical values)



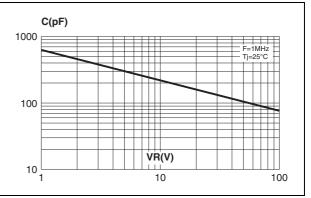
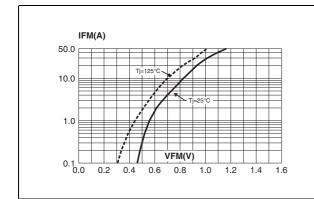
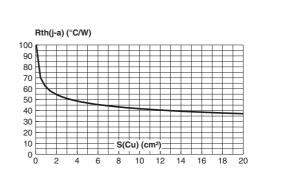


Figure 9. Forward voltage drop versus forward current (maximum values)

Figure 10. Thermal resistance junction to ambient versus copper surface under tab (Epoxy printed circuit board, copper thickness: 35 µm)





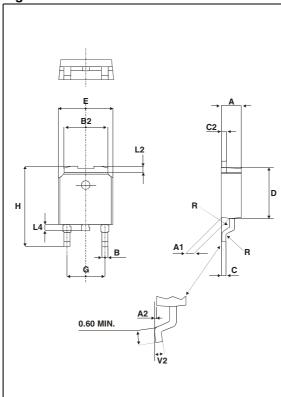
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STPS5H100 Package information

# 2 Package information

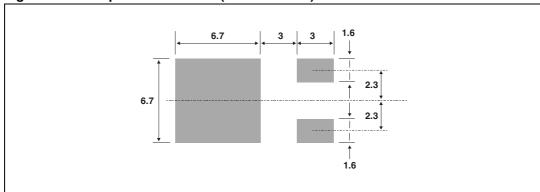
- Cooling method: by conduction (C)
- Epoxy meets UL94, V0

Figure 11. DPAK dimensions



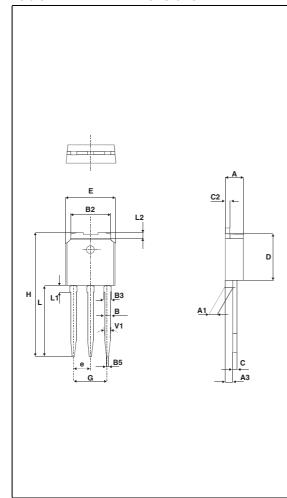
	Dimensions				
Ref	Millim	neters	Inc	hes	
	Min.	Max.	Min.	Max.	
Α	2.20	2.40	0.086	0.094	
A1	0.90	1.10	0.035	0.043	
A2	0.03	0.23	0.001	0.009	
В	0.64	0.90	0.025	0.035	
B2	5.20	5.40	0.204	0.212	
С	0.45	0.60	0.017	0.023	
C2	0.48	0.60	0.018	0.023	
D	6.00	6.20	0.236	0.244	
Е	6.40	6.60	0.251	0.259	
G	4.40	4.60	0.173	0.181	
Н	9.35	10.10	0.368	0.397	
L2	0.80 typ.		0.03	1 typ.	
L4	0.60	1.00	0.023	0.039	
V2	0°	8°	0°	8°	

Figure 12. Footprint dimensions (in millimeters)



Package information STPS5H100

Table 4. IPAK Dimensions



			Dimer	nsions		
Ref.	M	illimete	rs		Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	2.20		2.40	0.086		0.094
A1	0.90		1.10	0.035		0.043
А3	0.70		1.30	0.027		0.051
В	0.64		0.90	0.025		0.035
B2	5.20		5.40	0.204		0.212
В3			0.95			0.037
B5		0.30			0.035	
С	0.45		0.60	0.017		0.023
C2	0.48		0.60	0.019		0.023
D	6		6.20	0.236		0.244
Е	6.40		6.60	0.252		0.260
е		2.28			0.090	
G	4.40		4.60	0.173		0.181
Н		16.10			0.634	
L	9		9.40	0.354		0.370
L1	0.8		1.20	0.031		0.047
L2		0.80	1		0.031	0.039
V1		10°			10°	

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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# 3 Ordering information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STPS5H100B	S5H100	DPAK	0.30 g	75	Tube
STPS5H100B-TR	S5H100	DEAR	0.30 g	2500	Tape and reel
STPS5H100H	S5H100H	IPAK	0.40 g	75	Tube

# 4 Revision history

Date	Revision	Description of changes
Jul-2003	6B	Last issue.
03-Nov-2005	7	DPAK footprint dimensions updated.
15-Feb-2006	8	ECOPACK statement added.
05-Mar-2007	9	IPAK package added.

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